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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/050,600	01/18/2002	Yoshitaka Fujita	P14979-A	4645
21254 7	590 11/01/2006	EXAMINER		
	TELLECTUAL PRO	SHAND, ROBERTA A		
SUITE 200	8321 OLD COURTHOUSE ROAD SUITE 200		ART UNIT	PAPER NUMBER
VIENNA, VA	22182-3817		2616	

DATE MAILED: 11/01/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)			
Office Action Summary		10/050,600	FUJITA, YOSHITAKA			
		Examiner	Art Unit			
		Roberta A. Shand	2616			
Period fo	The MAILING DATE of this communication approximation of the second section and the second	opears on the cover sheet with the	e correspondence address			
WHIC - Exte after - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR REP CHEVER IS LONGER, FROM THE MAILING Insions of time may be available under the provisions of 37 CFR 1 SIX (6) MONTHS from the mailing date of this communication. O period for reply is specified above, the maximum statutory period tre to reply within the set or extended period for reply will, by statute to reply within the set or extended period for reply will, by statute to reply received by the Office later than three months after the mailed patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS COMMUNICATION .136(a). In no event, however, may a reply be d will apply and will expire SIX (6) MONTHS fro tte, cause the application to become ABANDO	ON. Itimely filed om the mailing date of this communication. NED (35 U.S.C. § 133).			
Status						
1)[\]	Responsive to communication(s) filed on 28	July 2006				
2a)□	This action is FINAL . 2b)⊠ This action is non-final.					
3)	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
-,	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.					
Disposit	ion of Claims	,				
4)⊠	☑ Claim(s) <u>1-27</u> is/are pending in the application.					
	4a) Of the above claim(s) is/are withdrawn from consideration.					
	☑ Claim(s) 11-16 and 23-27 is/are allowed.					
· · · · · · · · · · · · · · · · · · ·	•					
7)	Claim(s) is/are objected to.					
8)	Claim(s) are subject to restriction and	or election requirement.				
Applicati	ion Papers					
9)[]	The specification is objected to by the Examir	ner				
10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.						
,	•					
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a). Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).						
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
	under 35 U.S.C. § 119	·				
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).						
	☐ All b)☐ Some * c)☐ None of:	in priority under 33 0.3.6. § 119	(4)-(4) 01 (1).			
۵٫۱	1. Certified copies of the priority documents have been received.					
	 Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). 					
* See the attached detailed Office action for a list of the certified copies not received.						
		se of the certified copies not recei				
A44	·					
Attachment(s) 1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413)						
	e of References Cited (P10-692) e of Draftsperson's Patent Drawing Review (PT0-948)	4) 🔲 Interview Summa Paper No(s)/Mail				
3) Information Disclosure Statement(s) (PTO/SB/08) 5) Notice of Informal Patent Application						
Paper No(s)/Mail Date 6)						

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Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 2. Claims 1, 2, 6 and 7 are rejected under 35 U.S.C. 102(e) as being anticipated by Athreya (U.S. 2002/0027906 A1).
- Regarding claims 1 and 6, Athreya teaches (fig. 4) a multiplexing method of multiplexing signals from a transmitting section to a receiving section, comprising: adding to each of the signals an identification address (MAC address) identifying section through which the signal passes in a multiplexing system (IP multiplex) and outputting the signals (paragraph 42); extracting the address (fig. 3A and paragraph 43) and multiplexing the signals on the basis of the address (paragraph 42).
- 4. Regarding claims 2 and 7, Athreya teaches (fig. 4) the communication signal includes [[is]] a PPP packet created for each Internet subscriber apparatus, and the identification address includes [[is]] a MAC address.

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5. Claims 3-5 and 8-10 are rejected under 35 U.S.C. 102(e) as being anticipated by O'Dell (U.S. 2005/0157711 A1).

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- 6. Regarding claims 3 and 8, O'Dell teaches (fig. 1) a demultiplexing method of demultiplexing signals and transmitting to the receiving section, comprising: adding (fig. 3) to each of the signals an identification address (MAC address) identifying section through which the signal passes in a multiplexing system and outputting the signals; extracting the address and demultiplexing the multiplexed signals on the basis of the address (fig. 2 and paragraphs 46-47).
- 7. Regarding claims 4 and 9, O'Dell teaches (paragraphs 46-47) the communication signal includes [[is]] a PPP packet created for each Internet subscriber apparatus, and the identification address includes [[is]] a MAC address.
- 8. Regarding claims 5 and 10, O'Dell teaches (fig. 4) a demultiplexing method of demultiplexing a multiplexed signal, comprising: extracting the IP address (fig. 2) from each packet in the received multiplexed signal; demultiplexing the multiplexed signal into PPP packets (paragraphs 42-45) on the basis of the extracted address.

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Claim Rejections - 35 USC § 103

9. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 10. Claims 17-19 are rejected under 35 U.S.C. 103(a) as being unpatentable over O'Dell in view of Athreya.
- Regarding claim 17, O'Dell teaches (fig. 4) a multiplexing/demultiplexing apparatus that multiplexes signals from a transmitting section to a receiving section and demultiplexing signals and transmitting to the receiving section, comprising: extracting means for extracting (fig. 2) the address added (fig. 2) to the multiplexed signals identifying the section through which the signal passes in a demultiplexing system; and demultiplexing means demultiplexing the signals based on the MAC address (fig. 2, and paragraphs 46-47).
- 12. Although O'Dell teaches a multiplexing apparatus, O'Dell does not explicitly teach extracting means for extracting the address added to each of the signals an identification address identifying section through which the signal passes in a multiplexing system and outputting the signals; and multiplexing the signals on the basis of the address.
- Athreya teaches (fig. 4) adding to each of the signals an identification address (MAC address) identifying section through which the signal passes in a multiplexing system (IP multiplex) and outputting the signals (paragraph 42); extracting the address (fig. 3A and

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paragraph 43) and multiplexing the signals on the basis of the address (paragraph 42). It would have been obvious to one of ordinary skill in the art to adapt this to O'Dell's system to forward data without the use of routers (paragraph 41).

- 14. Regarding claims 18 and 19, Athreya teaches (fig. 2B) the packets are Ethernet IEEE 802.3 packets.
- 15. Claims 20-22 are rejected under 35 U.S.C. 103(a) as being unpatentable over O'Dell in view of Athreya further in view of Miki (U.S. 2004/0264505 A1).
- Regarding claim 20, O'Dell teaches (fig. 4) a multiplexing/demultiplexing apparatus that multiplexes signals from a transmitting section to a receiving section and demultiplexing signals and transmitting to the receiving section, comprising: extracting means for extracting (fig. 2) the address added (fig. 2) to the multiplexed signals identifying the section through which the signal passes in a demultiplexing system; and demultiplexing means demultiplexing the signals based on the MAC address (fig. 2, and paragraphs 46-47).
- 17. Although O'Dell teaches a multiplexing apparatus, O'Dell does not explicitly teach extracting means for extracting the address added to each of the signals an identification address identifying section through which the signal passes in a multiplexing system and outputting the signals; and multiplexing the signals on the basis of the address.

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18. Athreya teaches (fig. 4) adding to each of the signals an identification address (MAC address) identifying section through which the signal passes in a multiplexing system (IP multiplex) and outputting the signals (paragraph 42); extracting the address (fig. 3A and paragraph 43) and multiplexing the signals on the basis of the address (paragraph 42). It would have been obvious to one of ordinary skill in the art to adapt this to O'Dell's system to forward data without the use of routers (paragraph 41).

- 19. O'Dell and Athreya do not teach PoS.
- 20. Miki teaches (paragraph 13) PoS. It would have been obvious to one of ordinary skill in the art to adapt POS to O'Dell and Athreya's system because PoS is a highly scalable protocol that overcomes many of the inefficiencies of ATM, while providing legacy support to internetworks with existing SONET architectures. PoS provides a mechanism to carry packets directly within the SONET synchronous payload envelope (SPE) using a small amount of High-Level Data Link Control (HDLC) or PPP framing.
- 21. Regarding claims 21 and 22, Athreya teaches (fig. 2B) the packets are Ethernet IEEE 802.3 packets.

Allowable Subject Matter

22. Claims 11-16 and 23-27 are allowed.

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Response to Arguments

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23. Applicant's arguments with respect to claims 1-10 and 17-22 have been considered but

are moot in view of the new ground(s) of rejection.

Conclusion

24. Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Roberta A Shand whose telephone number is 571-272-3161. The

examiner can normally be reached on M-F 9:00am-5:30pm.

25. If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Huy Vu can be reached on 571-272-3155. The fax phone number for the

organization where this application or proceeding is assigned is 571-273-8300.

26. Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Roberta A Shand Examiner Art Unit 2665

HUY D. VU

SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2600